

CPEN 315L - Digital System Design Lab Spring 2008

Instructor: Dr. C. Gerousis
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Office Hours: I will be available in my office for consultation and questions during the hours shown below.

M: 11:00 am – 12:00 pm
W: 11:00 am – 12:00 pm
F: 2:00 pm – 3:00 pm
and by appointment only.

Web: <http://www.pcs.cnu.edu/~gerousis>

Text: None, only lab handouts.

Catalog Description

Combinational and sequential circuit design, implementation and simulation. The use of modern Electronic Design Automation (EDA) tools is emphasized.

Lab Topics

1	Combinational Circuit Design
2	Verilog Simulation
3	Sequential Circuit Design
4	Register Transfer
5	ALU Design
6	Final Project

Grading

The final lab grade will be determined as follows:

Labs	80%
Final Project	20%

Late Lab Policy

In general, late labs will not be accepted except in cases of documented illness or emergency. If you need to miss a class or a lab for a *legitimate reason*, you need to let me know before hand.

Academic Integrity

The students and faculty of Christopher Newport University have instituted a strict honor code:

On my honor, I will maintain the highest possible standards of honesty, integrity and personal responsibility. That means I will not lie, cheat, or steal and as a

member of this academic community, I am committed to creating an environment of respect and mutual trust.

This class will be run under the aegis of this honor code. This means that, as a student, you are expected to abide by this policy. Specifically, this means that you agree not to cheat in this class. Academic integrity means that you will be honest and straightforward in conduct in the class; you will treat your classmates and instructors with respect.

Disability Accommodation

If you believe you have a disability, you should make an appointment to see me to discuss your needs. In order to receive an accommodation, your disability must be on record in Disability Services located in the Academic Advising Center, Room 125, Administration Building, (594-8763).