

## **EXAM 2 – Study Guide**

### **Chapters 8 & 9**

- DRAM types: SDRAM, DDR SDRAM, RDRAM
- Memory READ timing diagrams, Memory bandwidth (see examples in notes).
- Connecting RAM chips to realize larger memory module (see HW).
- Coincident decoding (see problem 8-3).
- ALU design (see HW).
- Datapath: performing a micro-operation on a block diagram of a generic datapath
- Specifying the 16-bit control word to implement microoperation(s) (see HW).
- Manual simulation of a sequence of instructions (see HW)
- Simple Verilog 'Timing Diagram' question.