

FINAL EXAM GUIDELINES

You should focus on the following topics:

- Analysis and design of sequential circuits (using *D*, and *JK Flip-Flops*)
- Design of combinational logic circuits (see exam 2)
- Logic circuit design using decoders and/or multiplexers.
- Register design using multiplexer and D-flip-flop
- Shift registers (serial adder)
- Memory cell / Memory types (see last homework)
- Programming ROM (see last homework)
- Programming PLA (see last homework)
- Counter design (using *D flip-flop*)
- Short answers

Note that if you have reviewed and understood the homework problems and class notes, you should be ready for the exam.

Please note that there will be no makeup should you miss the final exam unless there is a documented illness or emergency situation.