

The second exam will cover chapters 4 and 5

You should focus on the following:

- Combinational logic circuit design at the block level (FA and HA). There will not be any analysis of combinational circuits.
- Function implementation using decoders and multiplexers.
- Sequential circuits/elements:  $D$  and  $JK$  flip-flop including timing diagram for  $D$  flip-flop.
- Sequential circuits (Finite State Machines) analysis with  $D$  and  $JK$  flip-flops.
- Short answers (review lecture notes).

*Note that if you have reviewed and understood the homework problems and class notes, you should be ready for the exam.*

***Please note that there will be not be any makeup should you miss the exam unless there is a documented illness or an emergency situation.***